

		LOVAG DECISION SHEET (LDS)		N°[LDS202]
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References	Standard(s) (incl. year)	[IEC 60947-3: 2010+A1:2012+A2:2016]		Subclause(s): 8.3.3.3
	Subject	[Making and breaking capacities Test values and conditions]	Submitted by: Lab. IK01 (Intek)	Date: 09/04/2019 [
Question	<p>It is not clear how the values of the shunt resistors, to be installed in parallel with the inductors to derive 0.6% of the current, should be calculated (EN 60947-1 par. 8.3.3.5.2 point d). In fact taking into account that we are dealing with a direct current, voltage drop across the inductor it is present only during the transient period and, at full current, the drop will be "zero".</p>			
Analysis	<p>The representatives of the laboratories around the table declare a different use of the resistance to be inserted in the test circuit for the derivation of the current: - "ac system" require the use of the resistors - "dc system" does not require the use. It is clear to everyone that present text of standard is built for tests in "ac system" and "arranged" to include tests in "dc system" (see also test circuits).</p>			
Decision	<p>It was decided that the ACAE Technical Committee members will ask to put this subject into the agenda of the Italian Sub Committee 121A next meeting. This should generate a comment with request for clarification directly to the international work group (IEC SC121A / MT6).</p>			
Date:		Prepared by: Lab. IK01 (Intek)		Approved by: